

FIG.1

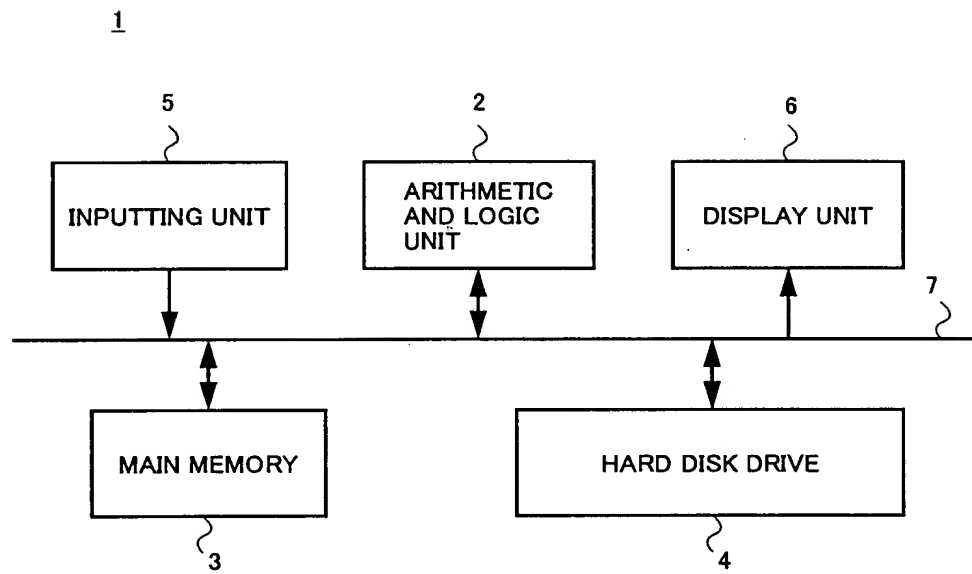
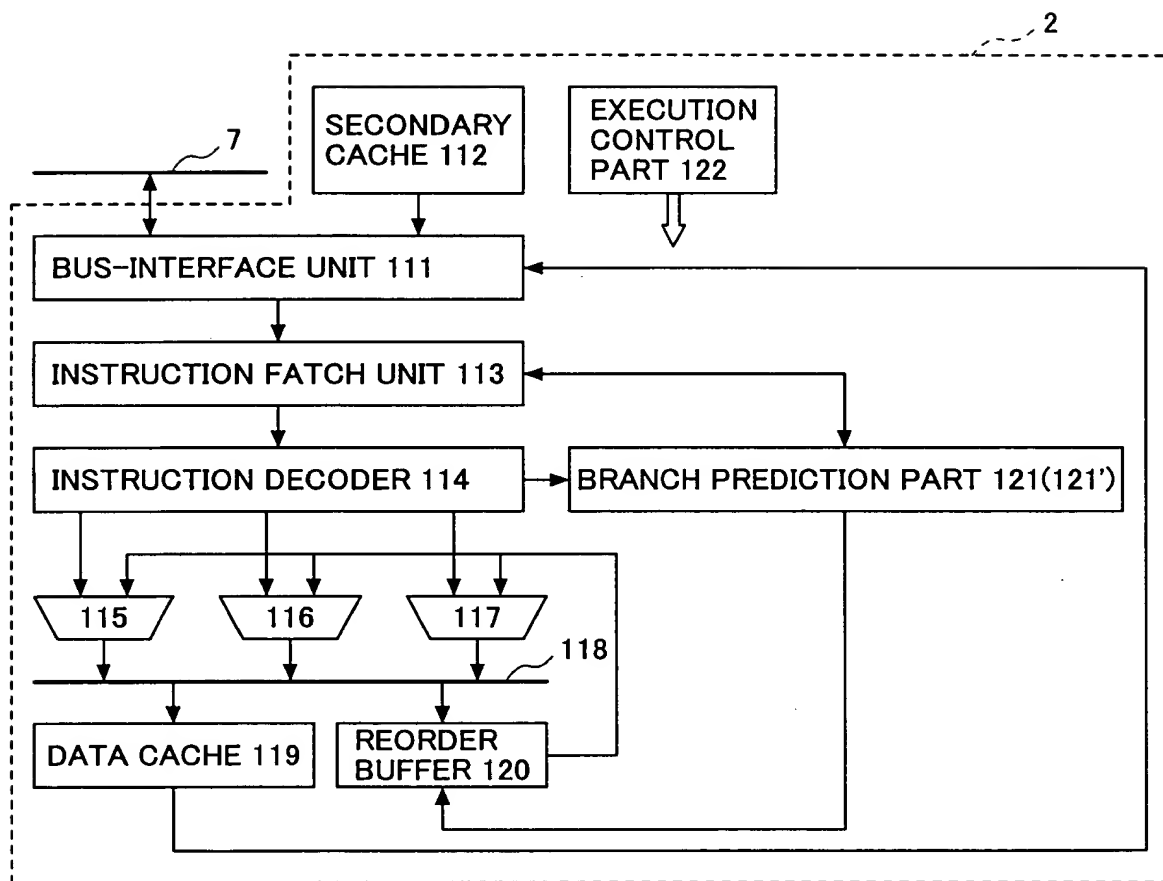
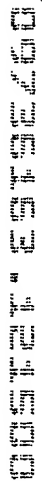
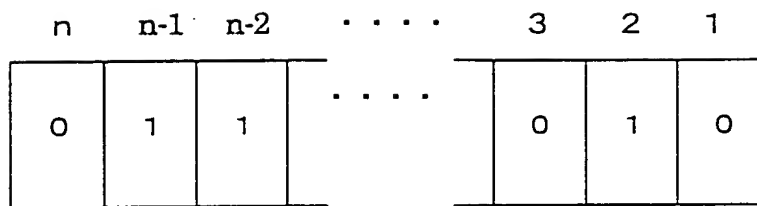


FIG.2



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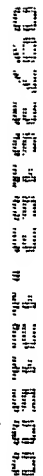


FIG. 7

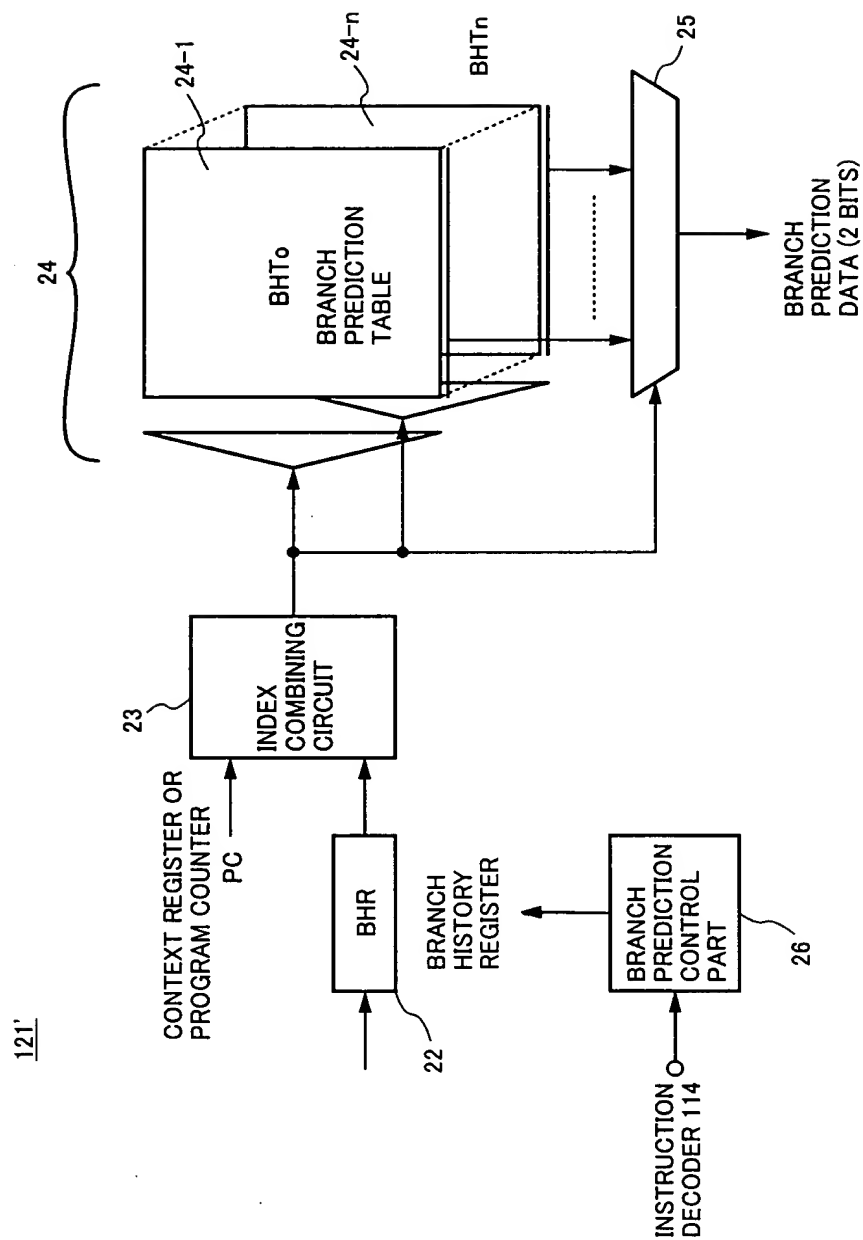


FIG.8

	t1	t2	t3	t4	t5		
A-BRANCH	TAKEN	→ TAKEN	→ NOT TAKEN	→ TAKEN	→ NOT TAKEN		
	0	0	0	0	0	0	1
	0	0	0	0	0	1	1
	0	0	0	0	1	1	0
	0	0	0	1	1	0	1
	0	0	0	1	1	0	0

a	b	c
ax	b1	01
ax	b2	00
ax	b3	
⋮	⋮	⋮
ax	bm	11

2 5 - X

FIG. 10

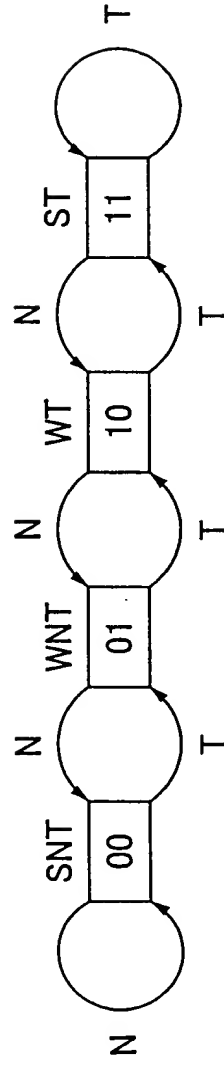


FIG.11

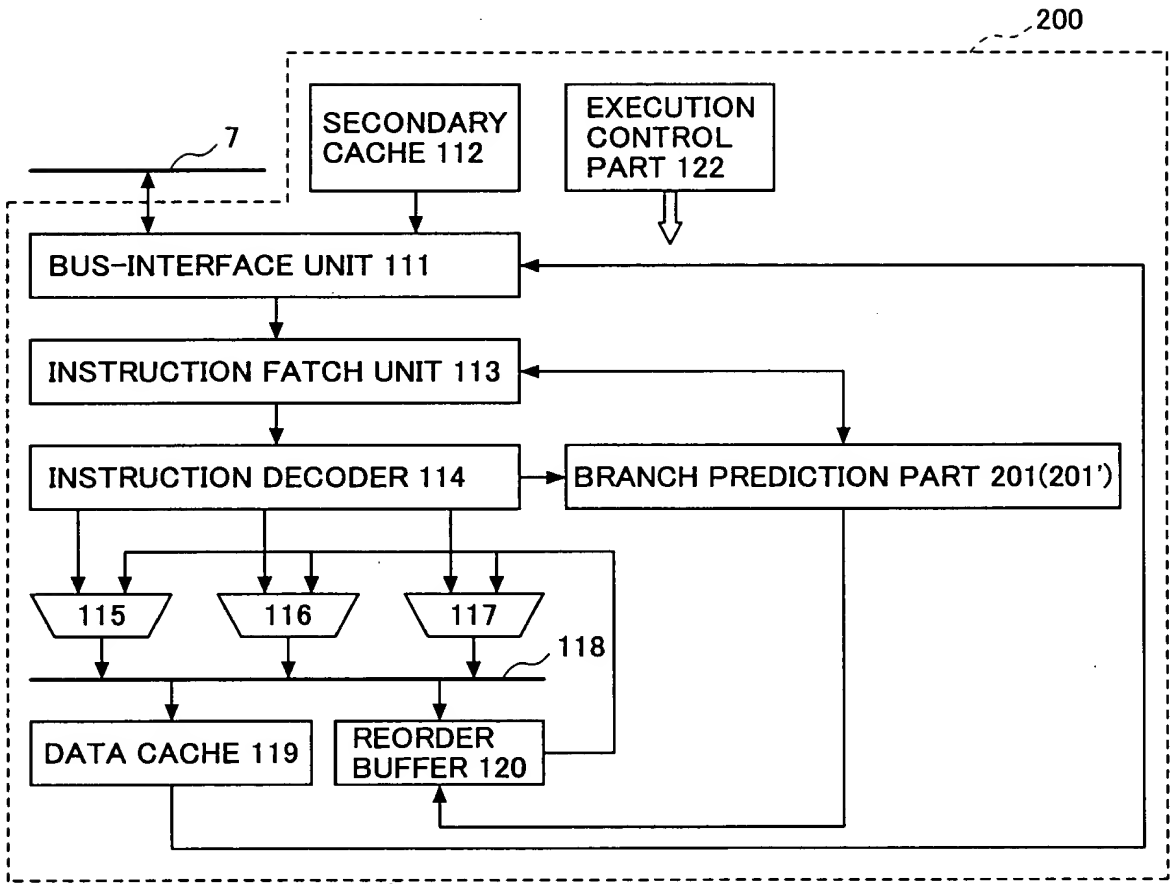


FIG.12

201

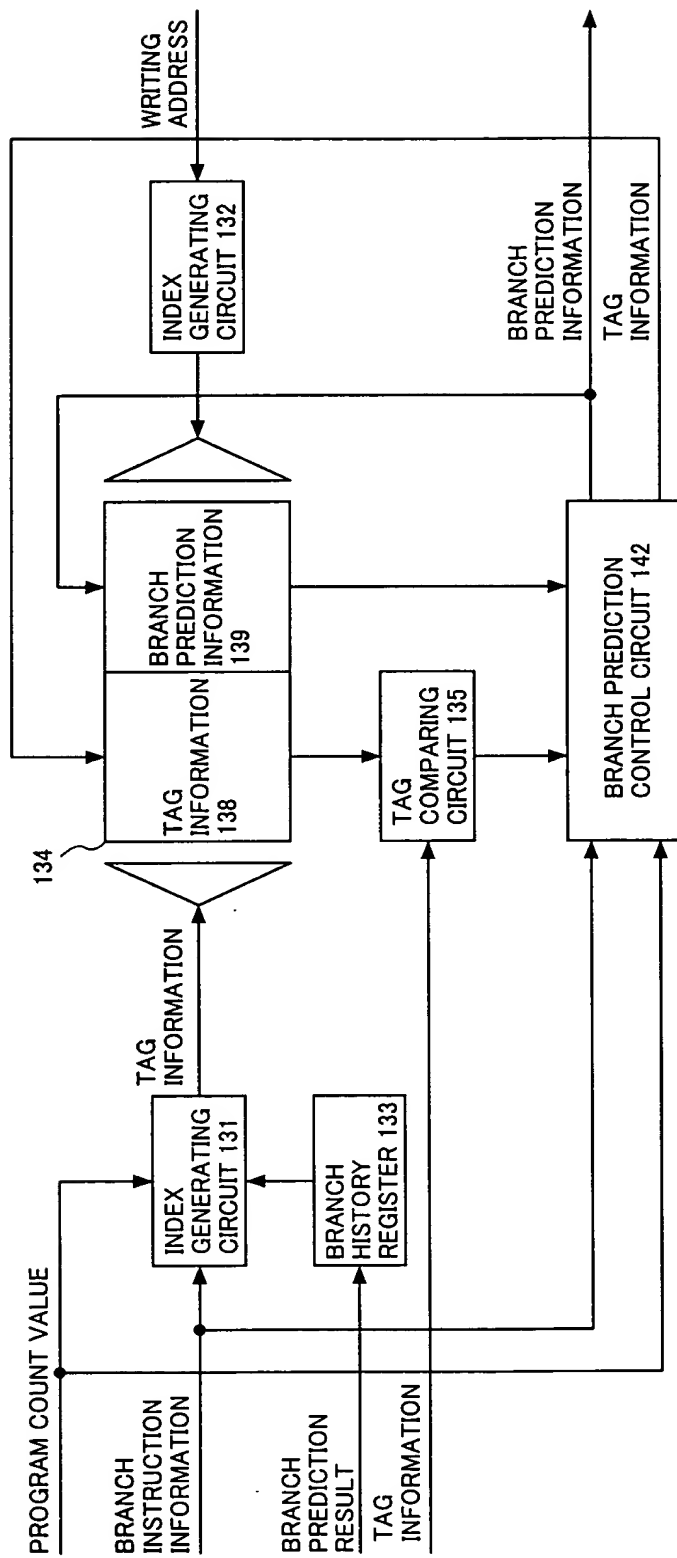


FIG.13

142

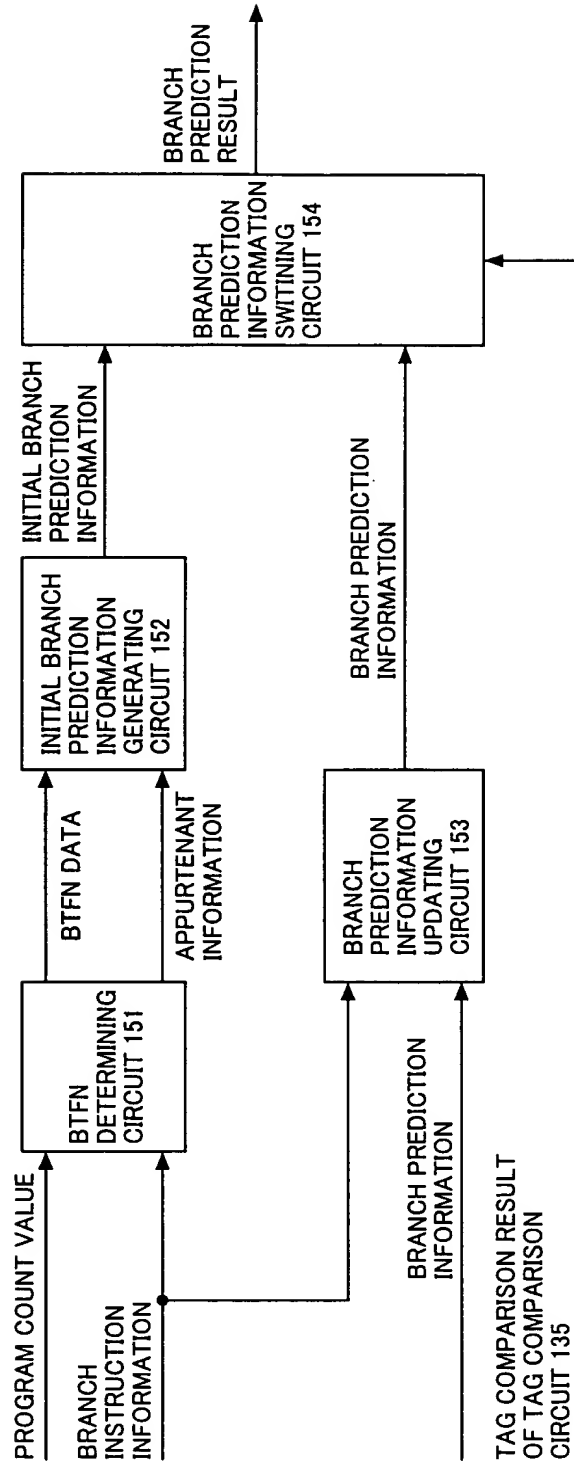


FIG. 14

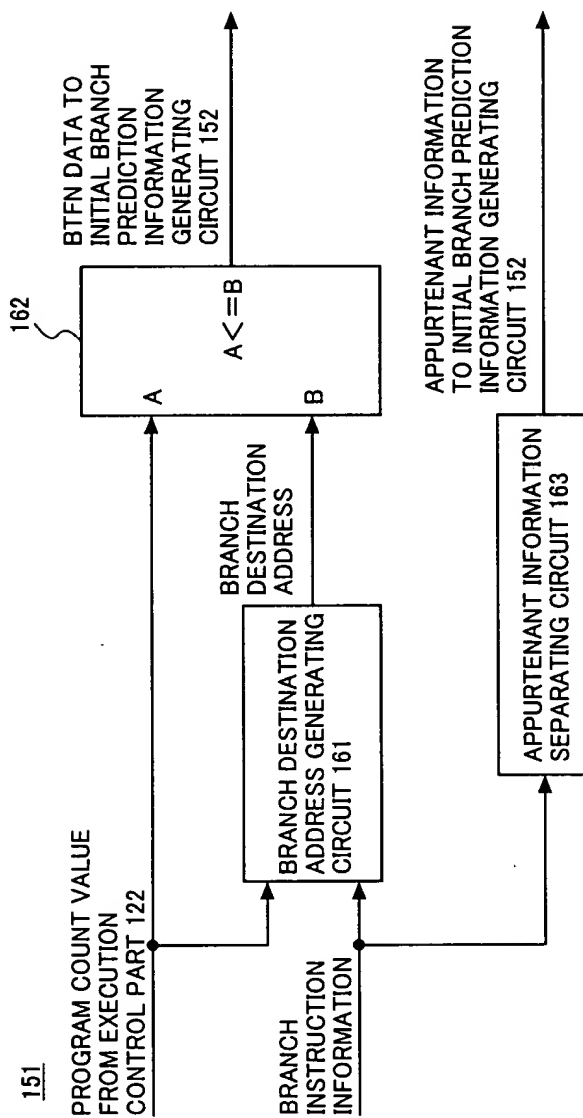


FIG.15

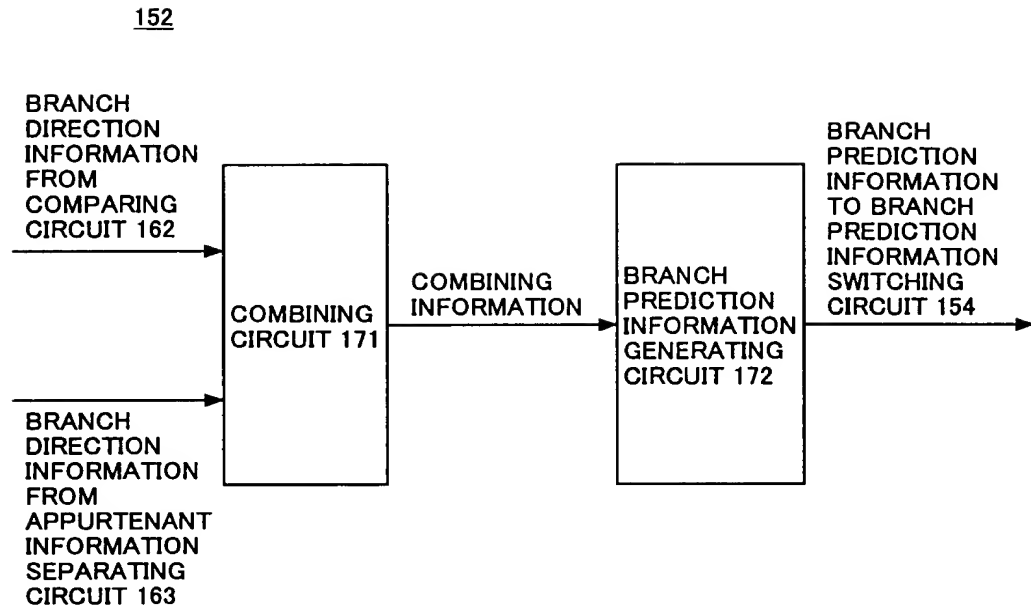


FIG.16

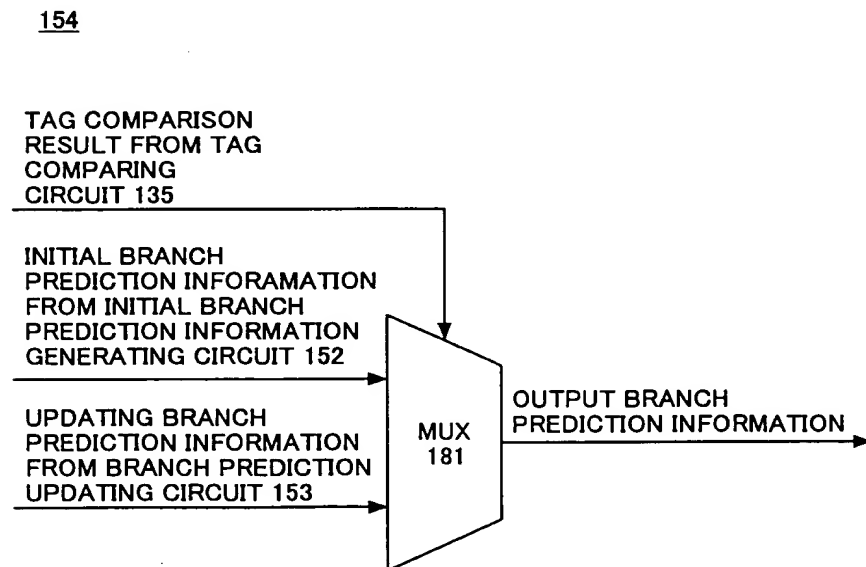


FIG.18

n BITS

r								s							
0 0 0 0 0 0 0 0								s1							
0 0 0 0 0 0 0 1								s2							
204-X															
⋮								⋮							
0 1 1 1 1 1 1 1								st-1							
1 1 1 1 1 1 1 1								st							

FIG.19

205-1

r	OUTPUT				
	00	01	10	11	
00	1	0	0	0	205-m
01	0	0	1	0	
10	0	0	1	0	
11	0	0	1	0	

205-X

FIG. 21

